Application Number

First Named Inventor

Filing Date

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FORM

12-04-06

PTO/SB/21 (09-06)

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10/796,502

March 8, 2004

Vaughn Betz (As Amended)

			,	Art Unit	2116	2116		
(to be used for all correspondence after initial filing)			Examiner Name	Mich	chael J. Brown			
Total Number of Pages in This Submission			Attorney Docket Number	0001	0174-0308-101			
ENCLOSURES (Check all that apply)								
	Fee Tran	nsmittal Form	<u> </u>	Drawing(s)		After Allowance Communication to TC		
	F	Fee Attached Amendment/Reply After Final Affidavits/declaration(s) Extension of Time Request		Licensing-related Papers Petition Petition to Convert to a		Appeal Communication to Board of Appeals and Interferences		
\boxtimes	Amendm					Appeal Communication to TC		
	F			Provisional Application		(Appeal Notice, Brief, Reply Brief)		
	<i>,</i>			Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer		Proprietary Information		
	Extensio					Status Letter Other Enclosure(s) (please Identify		
	Express	Abandonment Request		Request for Refund		below): Return Receipt Postcard		
	Supplemental Information		CD, Number of CD(s)					
	Disclosur	re Statement	Landscape Table on CD					
Certified Copy of Priority Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53			Remarks The Director is hereby authorized to charge payment of any fees or credit any overpayments required in connection with this Reply to Office Action to Deposit Account No. 06-1075, Order No. 000174-0308-101. A duplicate copy of this Transmittal is enclosed herewith.					
		SIGNAT	URE C	OF APPLICANT, ATTO	ORNEY	Y, OR AGENT		
Firm Name Fish & Neave IP Group, Ropes & Gray LLP								
Signat	ture	Polet R. C	7	leon				
Printed name Robert R. Jackson								
Date		November 30, 2006			Reg. No	Reg. No. 26,183		
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Date

000174-0308-101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Vaughn Betz (As Amended)

Application No.: 10/796,502 Confirmation No.: 3014

Filed : March 8, 2004

For : SYSTEMS AND METHODS FOR REDUCING

STATIC AND TOTAL POWER CONSUMPTION IN

PROGRAMMABLE LOGIC DEVICE

ARCHITECTURES

Group Art Unit : 2116

Examiner : Michael J. Brown

New York, New York 10020

November 30, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

Sir:

Responsive to the Notice of Non-Compliant Amendment mailed November 15, 2006, applicant hereby submits corrected Amendments to the Claims beginning on page 2 of this Response.

Remarks begin on page 21 of this Response.